

다우데이터 Ansys솔루션 정기 교육 안내

Ansys HFSS 3D Layout for PCB 기본 교육

교육비

- 일반 ₩600,000/1인
- 학생 ₩300,000/1인

일차	목차
1일차	<p>Module 01: Introduction</p> <ul style="list-style-type: none"> - Workshop 01.1: Connector on PCB Board Simulation - Workshop 01.2: SSN Simulation <p>Module 02: Viewing and Visibility</p> <ul style="list-style-type: none"> - Workshop 02.1: Differential Via Trace layer - Workshop 02.2: Differential via Pad stack - Workshop 02.3: Differential Via Construction <p>Module 03: Solvers, Meshing and Solution Setup</p> <ul style="list-style-type: none"> - Workshop 03.1: ECAD Xplorer to Import GDSII Foramt - Workshop 03.2: On-Chip Silicon Spiral Inductor <p>Module 04: HFSS 3D Layout Ports</p> <ul style="list-style-type: none"> - Workshop 04.1: Package Differential Pairs <p>Module 05: HFSS 3D Layout HFSS Extents</p> <ul style="list-style-type: none"> - Workshop 05.1: 62FR4 Microstrip <p>Module 06: Circuit Hierarchy</p> <ul style="list-style-type: none"> - Workshop 06.1: Differential Via Eye Diagram - Workshop 06.2: Differential Via TDR
2일차	<ul style="list-style-type: none"> - Workshop 07.1: Package-PCB Assembly and IBIS Setup - Workshop 08.1: Serial Channel Performance - Workshop 09.1: Connector on PCB Cutout - Workshop 09.2: 3D Components in HFSS 3D Layout - Workshop 10.1: Via Optimization on PCB - Workshop 10.2: Low Noise Amplifier and Board Simulation in HFSS 3D Layout - Workshop 11.1: ESD Simulation

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